

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki et al.      Art Unit : 2822  
Serial No. : 10/730,047      Examiner : Roy Karl Potter  
Filed : December 9, 2003      Confirmation No. 3013  
Title : SEMICONDUCTOR CHIP AND METHOD FOR MANUFACTURING THE  
SAME

**MAIL STOP AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Applicants request consideration of the references listed on the attached PTO-1449 form. Under 37 C.F.R. § 1.98 (a)(2)(ii), only copies of foreign patent documents and/or non-patent literature are enclosed. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

Submitted herewith is an English translation of the following foreign language references, or portions thereof:

| Desig. ID | Source  |
|-----------|---|
| ACC       | JAPAN 10-125930 – English Abstract: DIALOG(R) File 347:JAPIO (2002); Accession No. 05842830   |
| ADD       | JAPAN 10-125931 – English Abstract: DIALOG(R) File 352:Derwent WPI (2002); Accession No. 011762805  |
| AEE       | JAPAN 2000-061785 – English Abstract: esp@cenet database:<br><a href="http://v3.espacenet.com/textdoc?DB=EPODOC&amp;IDX=JP2000061785&amp;F=8">http://v3.espacenet.com/textdoc?DB=EPODOC&amp;IDX=JP2000061785&amp;F=8</a> ;<br>29 March 2005   |
| AFF       | JAPAN 2002-049359 – English Abstract: DIALOG(R) File 347:JAPIO (2004); Accession No. 07180968 & Concise Statement: This reference was cited in the specification of U.S. Patent Application Serial No. 10/774,700, filed on February 10, 2004, and describes, in a semiconductor device, a structure in which a substrate that becomes a panel includes both a pixel portion and a driving circuit that are integrally formed as an internal circuit and an external circuit that is formed outside of the panel. |
| AGG       | JAPAN 3408154 – Full English Translation  |

Applicant : Shunpei Yamazaki et al.  
Serial No. : 10/730,047  
Filed : December 9, 2003  
Page : 2 of 2

Attorney's Docket No.: 12732-185001 / US6821

This statement is being filed before the receipt of a first Office Action on the merits.  
Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

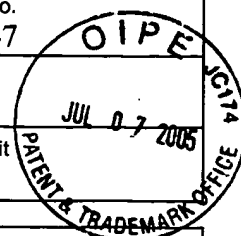
Date: July 7, 2005

  
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| Substitute Form PTO-1449<br>(Modified)<br><br><b>Information Disclosure Statement<br/>by Applicant</b><br>(Use several sheets if necessary)<br><br>(37 CFR §1.98(b)) | U.S. Department of Commerce<br>Patent and Trademark Office | Attorney's Docket No.<br>12732-185001 | Application No.<br>10/730,047 |
|  | Applicant<br>Shunpei Yamazaki et al.                       |                                       |                               |
|  | Filing Date<br>December 9, 2003                            | Group Art Unit<br>2822                |                               |



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| Examiner Initial | Desig. ID | Document Number | Publication Date | Patentee        | Class | Subclass | Filing Date If Appropriate |
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| Examiner Signature   | Date Considered |
| EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. |                 |

|  |  |                                       |                               |
|--|--|---------------------------------------|-------------------------------|
| Substitute Form PTO-1449<br>(Modified)   | U.S. Department of Commerce<br>Patent and Trademark Office | Attorney's Docket No.<br>12732-185001 | Application No.<br>10/730,047 |
| <b>Information Disclosure Statement<br/>by Applicant</b><br>(Use several sheets if necessary)<br>(37 CFR §1.98(b)) |  | Applicant<br>Shunpei Yamazaki et al.  |                               |
|  |  | Filing Date<br>December 9, 2003       | Group Art Unit<br>2822        |

| U.S. Patent Documents |           |                 |                  |                 |       |          |                            |
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| Examiner Initial      | Desig. ID | Document Number | Publication Date | Patentee        | Class | Subclass | Filing Date If Appropriate |
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| Foreign Patent Documents or Published Foreign Patent Applications |           |                 |                  |                          |       |          |             |    |
|---|-----------|-----------------|------------------|--------------------------|-------|----------|-------------|----|
| Examiner Initial  | Desig. ID | Document Number | Publication Date | Country or Patent Office | Class | Subclass | Translation |    |
|   |           |                 |                  |                          |       |          | Yes         | No |
|   | ACC       | 10-125930       | 05/15/1998       | JAPAN                    |       |          | AB          |    |
|   | ADD       | 10-125931       | 05/15/1998       | JAPAN                    |       |          | AB          |    |
|   | AEE       | 2000-061785     | 02/29/2000       | JAPAN                    |       |          | AB          |    |
|   | AFF       | 2002-049359     | 02/15/2002       | JAPAN                    |       |          | AB          |    |
|   | AGG       | 3408154         | 05/19/2003       | JAPAN                    |       |          | Full        |    |

| Other Documents (include Author, Title, Date, and Place of Publication) |           |   |
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| Examiner Initial  | Desig. ID | Document  |
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|   | AII       | "Two-way display developed"; Newspaper " <i>The Japan Times</i> "; July 3, 2003   |
|   | AJJ       | <i>Exhibition of Active Matrix Type Organic EL Display at "13<sup>th</sup> Flat Panel Display Manufacturing Technology Expo &amp; Conference"</i> by ELDis Group; July 2, 2003 (full translation) |
|   | AKK       | Documents distributed in the " <i>13<sup>th</sup> Flat Panel Display Manufacturing Technology Expo &amp; Conference</i> " by ELDis Group; July 3, 2003 (full translation)                         |

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